

**FIG. 1**

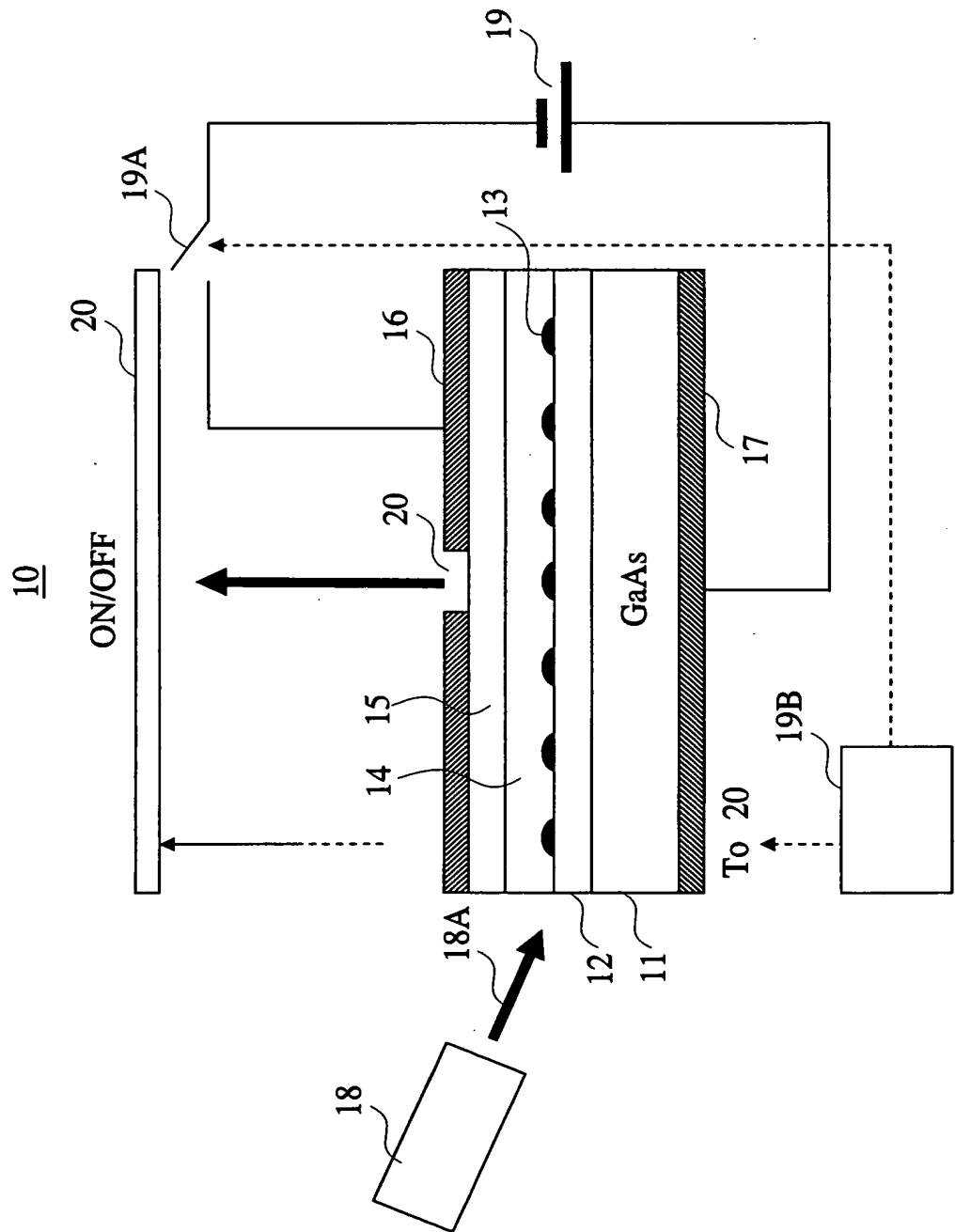


FIG.2

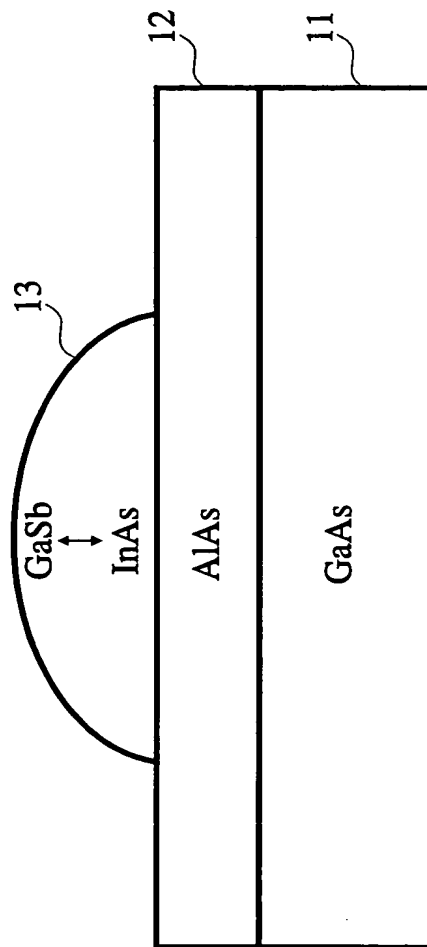


FIG.3A

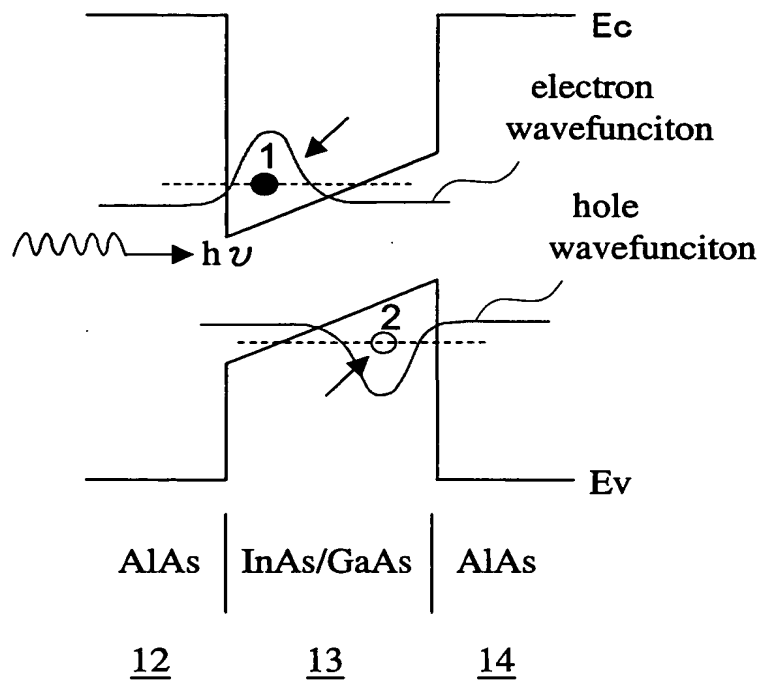


FIG.3B

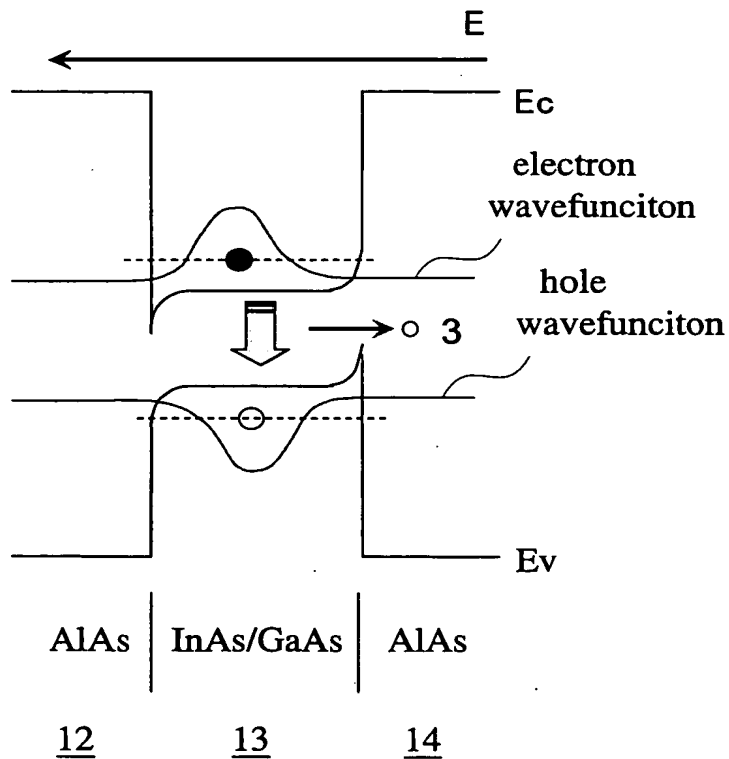
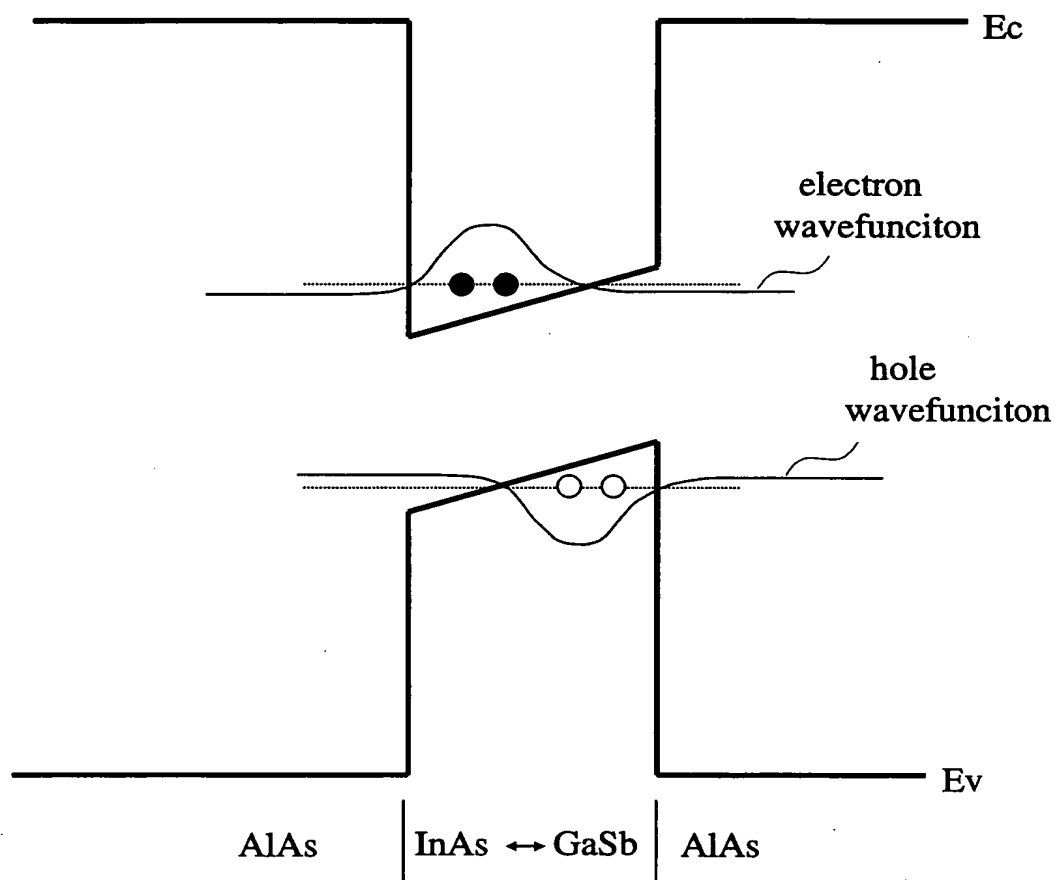


FIG.4



TIME



INCIDENT PULSE



RECOMBINATION  
PROBABILITY



APPLIED VOLTAGE



SHUTTER

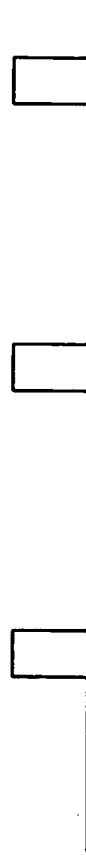


FIG.5A

FIG.5B

FIG.5C

FIG.5D

FIG.6

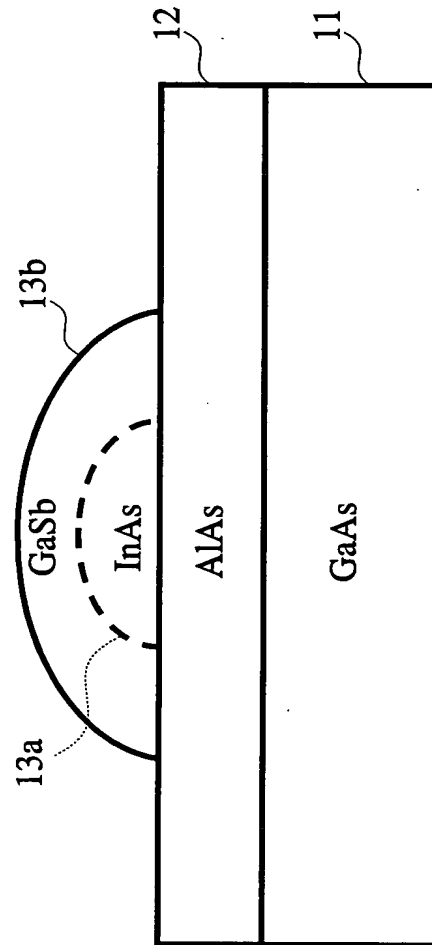


FIG. 7

